



Attorney's Docket No.: 09464-010001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Marco Zuniga et al.

Art Unit : 2838

Serial No. : 09/475,713

Examiner : Shawn Riley

Filed : December 30, 1999

Title : SWITCHING REGULATOR WITH MULTIPLE POWER TRANSISTOR
DRIVING VOLTAGES

Commissioner for Patents
Washington, D.C. 20231

RECEIVED
JAN 24 2001
TECHNOLOGY CENTER

#9/B
Hawkins
1/26/01

RESPONSE

In response to the action mailed September 11, 2000, please amend the application as follows:

In the Claims:

- B!
1. (Amended) A voltage regulator having an input terminal and an output terminal, comprising:
a first transistor connecting the input terminal to an intermediate terminal, the first transistor including a first gate oxide layer;
a second transistor connecting the intermediate terminal to ground, the second transistor including a second gate oxide layer;
a controller that drives the first and second transistors to alternately couple the intermediate terminal between the input terminal and ground, wherein the controller drives the first transistor with a first gate voltage and drives the second transistor with a second, different gate voltage; and
a filter disposed between the input terminal and the output terminal to provide a substantially DC voltage at the output terminal, the filter including at least one element connecting the intermediate terminal to the output terminal.

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

01/12/2001 RHARIS1 00000122 09475713

01 FC:202 80.00 0P
02 FC:203 36.00 0P

Date of Deposit

January 11, 2001

Signature

Susan Hendrick

Typed or Printed Name of Person Signing Certificate

Susan Hendrick